

**Dkt. No. BUR920000059US1**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant: Bryant *et al.***

**Group Art Unit: 2826**

**Filed: 6/21/2001**

**Examiner: Abraham, Fetsum**

**Serial No.: 09/886,823**

**Title: DOUBLE GATED TRANSISTOR AND METHOD OF FABRICATION**

**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

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**Request for Reconsideration**

**OCT 19 2004**

**Sir:**

**This Request for reconsideration is in response to the Office Action mailed**

**July 23, 2004.**

09/886,823

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